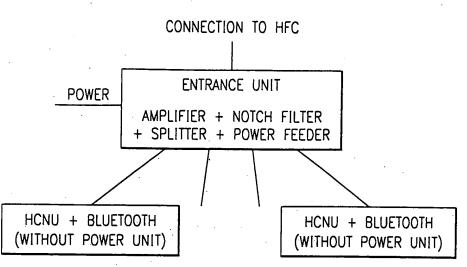
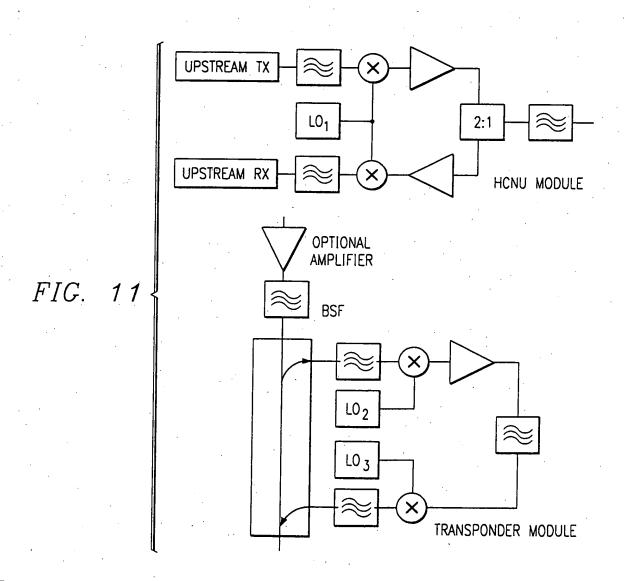
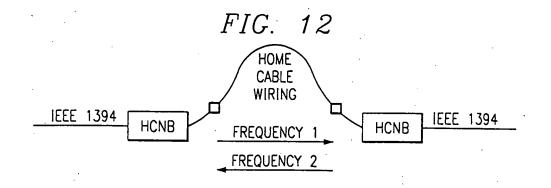
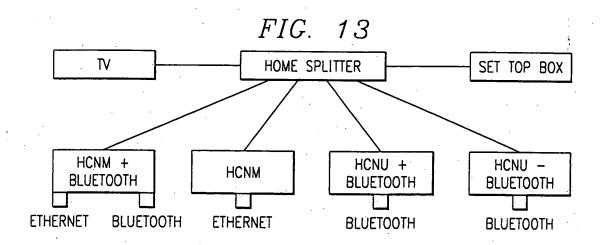


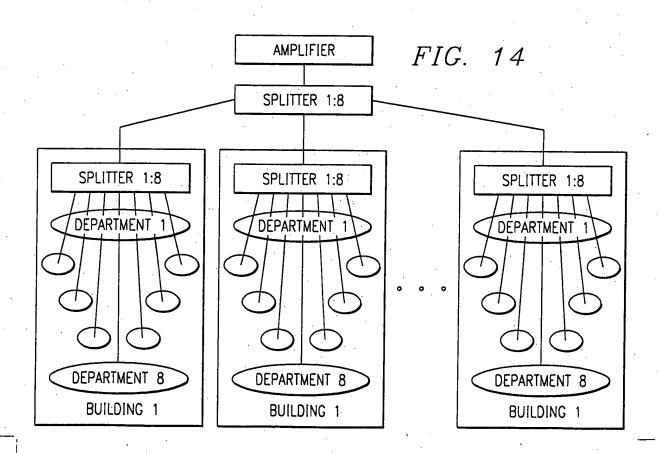
FIG. 10

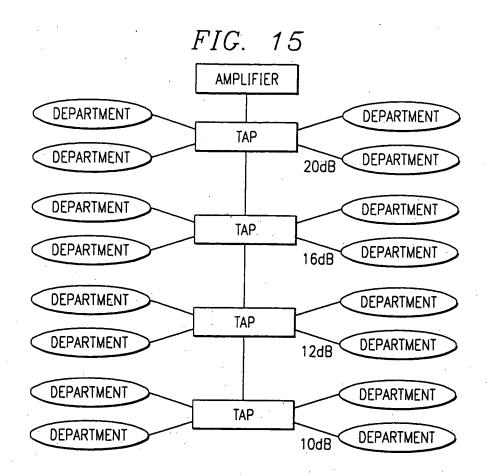


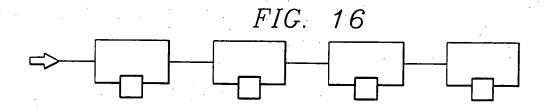












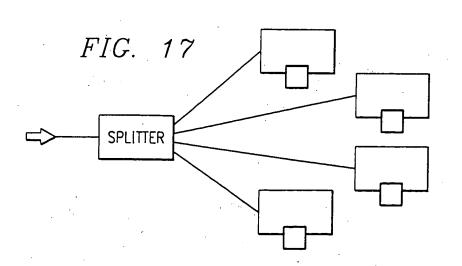


FIG. 18

ETHERNET

CPU 860 (OR ARM) DOCSIS 1.1

IEEE802.11MAC

4230

CM MAC

DOCSIS CHANNEL 4030 CM PHY

TUNER

HOME NETWORKING CHANNEL 4030

FPGA IEEE802.11MAC (HW PART) + GLUE LOGIC CM PHY 4521

4521 CMTS BURST RECEIVER 900MHz TX + RX FRONT END

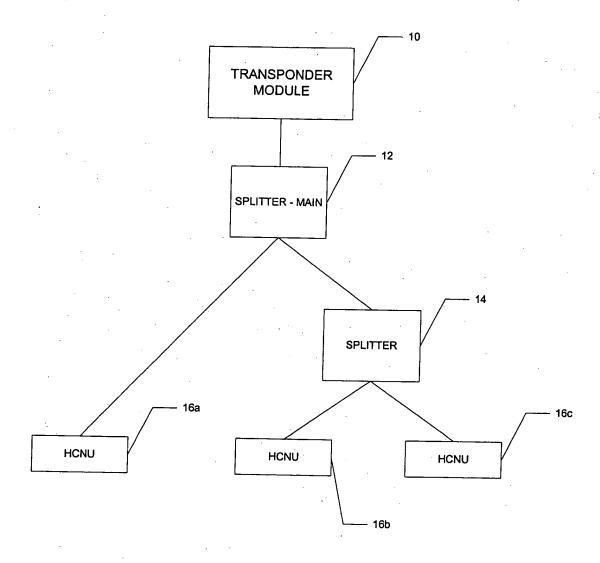
ETHERNET

CPU 860 (OR ARM) DOCSIS 1.1 IEEE802.11MAC FIG. 19

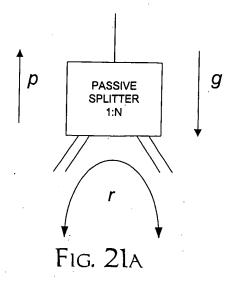
HOME NETWORKING CHANNEL

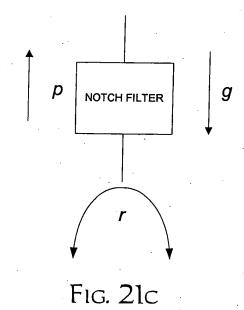
FPGA IEEE802.11MAC (HW PART) + GLUE LOGIC 4030 CM PHY

4521 CMTS BURST RECEIVER 900MHz TX + RX FRONT END



F1G. 20





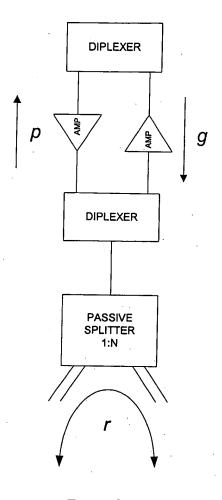


Fig. 21B